105 Load R4, [R30]
110 Add R5, (R32+R31)
115 Add R6, (R29+R5)
120 Load R10, (R4)

FIG. 1A (Prior Art)

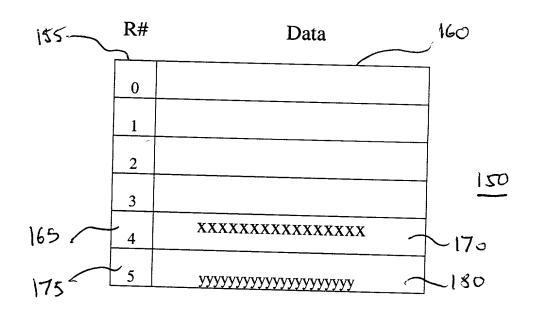


FIG. 1B (Prior Art)

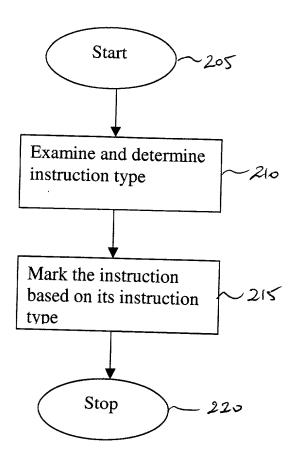


FIG. 2

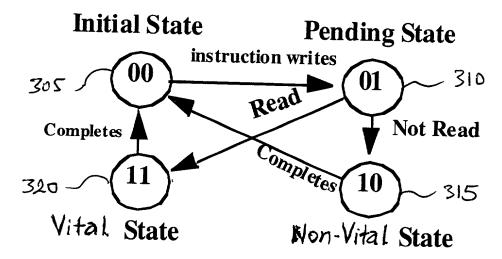


FIG. 3

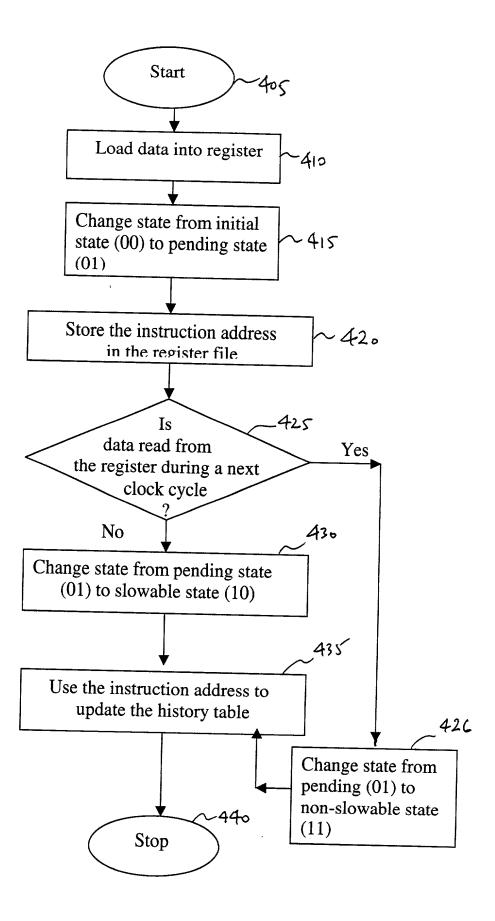


FIG. 4

	505 510	5	Instruction Address	720
R#_	505 510 Data	State	Instruction Address	/
	//			
0		00		
1		00		
2		00		
3		00		
4	XXXXXXXXXXXXXX	01	уууууууууу	
5		00		
6		00		
7		00		
8		00		
9		00		

500

FIG. 5

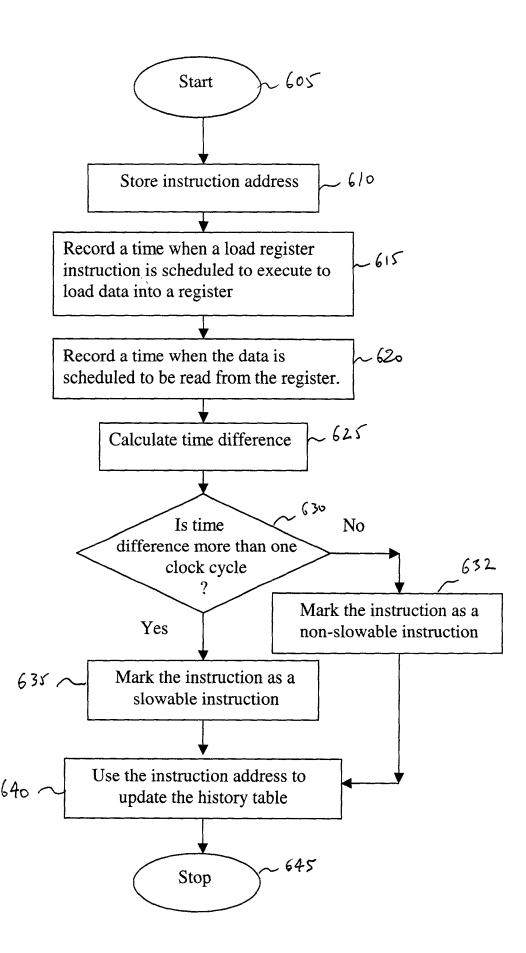


FIG. 6

R#	Data	WTS	RTS	SR	Instruction Address
0				0	
1				0	
2		.,		0	
3				0	
4	XXXXXXXXXXXXXX	56	60	1	УУУУУУУУУУУУ
5				0	
6				0	
7				0	
8				0	
9				0	
5	710	715 -	5 720	ر 725	730

FIG. 7

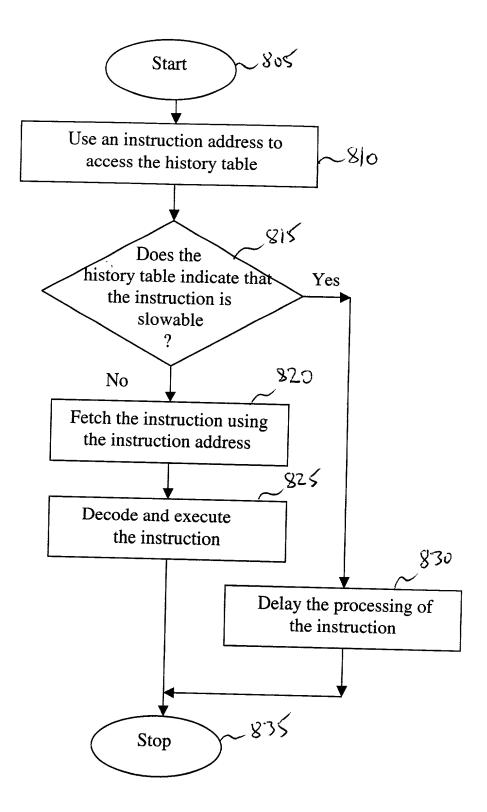


FIG. 8

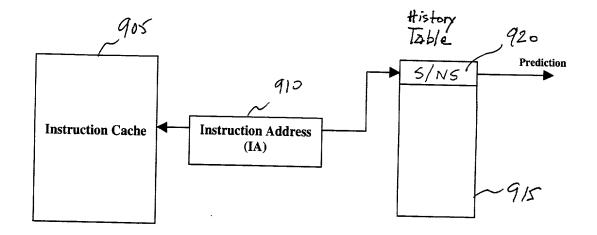


FIG. 9